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A: The file has two mime-types i.e. miff, pdf and x. You can either extract them or change the settings on your file manager (apparently your file manager does not recognize miff files and wants to open them with Preview on macOS) to show them as images. Open /usr/local/bin and run the command: sudo chmod a+rx $PATH This enables the execute bit for all commands within that directory. You will probably want to add this to a sudo crontab if you have a scheduled update. Run the command: PATH=/usr/local/bin:$PATH to reload the PATH you just set. Open System preferences and open the Security and Privacy settings page. Select File types and then click the Open window to locate and add both miff and pdf mime-types. 1. Field of the Invention The present invention relates generally to a wiring substrate mounted on a semiconductor package and, more particularly, to a high-density wiring substrate that includes protruding wires and a semiconductor package mounted thereon and a method of mounting the semiconductor package on the wiring substrate. 2. Description of the Related Art In recent years, as electronic devices have been made increasingly smaller, a semiconductor package mounted thereon and a wiring substrate mounted on a semiconductor package have been made smaller. In this manner, there have been the growing demands for miniaturization and high-density mounting of a semiconductor package and a wiring substrate. A variety of techniques have been proposed for the mounting structure of a semiconductor package and a wiring substrate. Japanese Patent Application Laid-Open Publication No. 2005-210052, for example, discloses a technique in which a metal cap is mounted on the upper surface of a semiconductor package, the metal cap is electrically connected to an underlying metal, and a high-density mounting substrate is mounted on the metal cap. Japanese Patent Application Laid-Open Publication No. 2006-299847 discloses a technique in which a high-density metal plate is mounted on a substrate and the mounting structure is mounted on a semiconductor package via a supporting member. Japanese Patent Application Laid-Open Publication No. 2007-167562 discloses a technique in which a substrate is used as a supporting member for supporting a semiconductor package, and a wiring substrate is mounted on the supporting member.Q:

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